100 mA Positive Voltage Regulators

The MC78L00A Series of positive voltage regulators are inexpensive, easy-to-use devices suitable for a multitude of applications that require a regulated supply of up to 100 mA. Like their higher powered MC7800 and MC78M00 Series cousins, these regulators feature internal current limiting and thermal shutdown making them remarkably rugged. No external components are required with the MC78L00 devices in many applications.

These devices offer a substantial performance advantage over the traditional zener diode-resistor combination, as output impedance and quiescent current are substantially reduced.

Features

- Wide Range of Available, Fixed Output Voltages
- Low Cost
- Internal Short Circuit Current Limiting
- Internal Thermal Overload Protection
- No External Components Required
- Complementary Negative Regulators Offered (MC79L00A Series)
- NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These are Pb–Free Devices

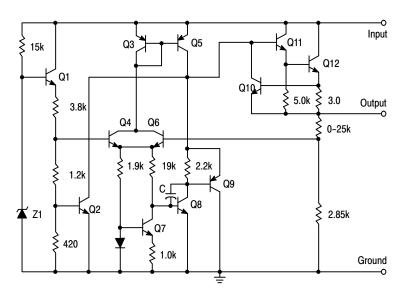
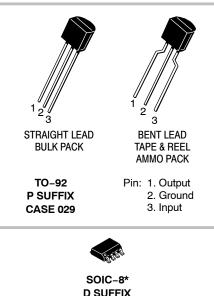


Figure 1. Representative Schematic Diagram



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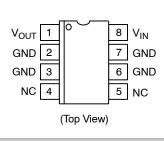
http://onsemi.com



*SOIC-8 is an internally modified SO-8 package. Pins 2, 3, 6, and 7 are electrically common to the die attach flag. This internal lead frame modification decreases package thermal resistance and increases power dissipation capability when appropriately mounted on a printed circuit board. SOIC-8 conforms to all external dimensions of the standard SO-8 package.

PIN CONNECTIONS

CASE 751



ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 9 of this data sheet.

DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 12 of this data sheet.

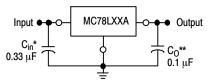


Figure 2. Standard Application

A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0 V above the output voltage even during the low point on the input ripple voltage.

* C_{in} is required if regulator is located an appreciable distance from power supply filter.

** C_O is not needed for stability; however, it does improve transient response.

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage (5.0 V–9.0 V) (12 V–18 V) (24 V)	VI	30 35 40	Vdc
Storage Temperature Range	T _{stg}	-65 to +150	°C
Maximum Junction Temperature	TJ	150	°C
Moisture Sensitivity Level	MSL	1	-
ESD Capability, Human Body Model (Note 1)	ESD _{HBM}	2000	V
ESD Capability, Machine Model (Note 1)	ESD _{MM}	200	V
ESD Capability, Charged Device Model (Note 1)	ESD _{CDM}	2000	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. This device series incorporates ESD protection and is tested by the following methods: ESD Human Body Model tested per AEC-Q100-002 (EIA/JESD22-A114)

ESD Human Body Model tested per AEC-Q100-002 (EIA/JESD22-A ESD Machine Model tested per AEC-Q100-003 (EIA/JESD22-A115)

ESD Charged Device Model tested per EIA/JES D22/C101, Field Induced Charge Model.

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Package Dissipation	PD	Internally Limited	W
Thermal Characteristics, TO-92 Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	200	°C/W
Thermal Characteristics, SOIC8 Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	Refer to Figure 8	°C/W

2. Thermal Resistance, Junction-to-Ambient depends on P.C.B. Copper area. See details in Figure 8.

Thermal Resistance, Junction-to-Case is not defined. SOIC 8 lead and TO-92 packages that do not have a heat sink like other packages may have. This is the reason that a Theta JC is never specified. A little heat transfer will occur through the package but since it is plastic, it is minimal. The majority of the heat that is transferred is through the leads where they connect to the circuit board.

ELECTRICAL CHARACTERISTICS (V _I = 10 V, I _O = 40 mA, C _I = 0.33 μ F, C _O = 0.1 μ F, -40°C < T _J < +125°C (for MC78LXXAB,
NCV78L05A), 0°C < T _J < +125°C (for MC78LXXAC), unless otherwise noted.)

		MC78L0	5AC, AB, NO	CV78L05A	
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	4.8	5.0	5.2	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$ 7.0 Vdc $\leq V_1 \leq 20 \text{ Vdc}$ 8.0 Vdc $\leq V_1 \leq 20 \text{ Vdc}$	Reg _{line}		55 45	150 100	mV
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}		11 5.0	60 30	mV
Output Voltage (7.0 Vdc \leq V _I \leq 20 Vdc, 1.0 mA \leq I _O \leq 40 mA) (V _I = 10 V, 1.0 mA \leq I _O \leq 70 mA)	Vo	4.75 4.75		5.25 5.25	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}		3.8 _	6.0 5.5	mA
Input Bias Current Change (8.0 Vdc $\leq V_1 \leq 20$ Vdc) (1.0 mA $\leq I_0 \leq 40$ mA)	ΔI _{IB}			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f⊡≤100 kHz)	V _n	-	40	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 8.0 Vdc \leq V _I \leq 18 V, T _J = +25°C)	RR	41	49	-	dB
Dropout Voltage (T _J = +25°C)	V _I – V _O	-	1.7	-	Vdc

NOTE: NCV78L05A: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV prefix is for automotive and other applications requiring site and change control.

ELECTRICAL CHARACTERISTICS (V_I = 14 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, -40°C < T_J < +125°C (for MC78LXXAB), 0°C < T_J < +125°C (for MC78LXXAC), unless otherwise noted.)

		MC78L08AC, AB			
Characteristics	Symbol	Min	Тур	Мах	Unit
Output Voltage (T _J = +25°C)	Vo	7.7	8.0	8.3	Vdc
Line Regulation (T _J = +25°C, I _O = 40 mA) 10.5 Vdc ≤ V _I ≤ 23 Vdc 11 Vdc ≤ V _I ≤ 23 Vdc	Reg _{line}		20 12	175 125	mV
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}		15 8.0	80 40	mV
Output Voltage (10.5 Vdc $\leq V_{I} \leq$ 23 Vdc, 1.0 mA $\leq I_{O} \leq$ 40 mA) (V _I = 14 V, 1.0 mA $\leq I_{O} \leq$ 70 mA)	Vo	7.6 7.6		8.4 8.4	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}		3.0 _	6.0 5.5	mA
Input Bias Current Change (11 Vdc \leq V _I \leq 23 Vdc) (1.0 mA \leq I _O \leq 40 mA)	Δl _{lB}			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f <u>]</u> ≤100 kHz)	V _n	_	60	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 12 V \le V _I \le 23 V, T _J = +25°C)	RR	37	57	-	dB
Dropout Voltage (T _J = +25°C)	V _I – V _O	-	1.7	-	Vdc

		MC78L09AC, AB			
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	8.6	9.0	9.4	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$ $11.5 \text{ Vdc} \le V_I \le 24 \text{ Vdc}$	Reg _{line}	_	20	175	mV
$12 \text{ Vdc} \le \text{V}_{\text{I}} \le 24 \text{ Vdc}$		-	12	125	
Load Regulation (T _J = +25°C, 1.0 mA ≤ I _O ≤ 100 mA) (T _J = +25°C, 1.0 mA ≤ I _O ≤ 40 mA)	Reg _{load}		15 8.0	90 40	mV
Output Voltage (11.5 Vdc \leq V _I \leq 24 Vdc, 1.0 mA \leq I _O \leq 40 mA) (V _I = 15 V, 1.0 mA \leq I _O \leq 70 mA)	Vo	8.5 8.5		9.5 9.5	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}		3.0 _	6.0 5.5	mA
Input Bias Current Change (11 Vdc ≤ V _I ≤ 23 Vdc) (1.0 mA ≤ I _O ≤ 40 mA)	Δl _{IB}			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f <u>⊡</u> ≤100 kHz)	V _n	-	60	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 13 V ≤ V _I ≤ 24 V, T _J = +25°C)	RR	37	57	-	dB
Dropout Voltage (T _J = +25°C)	V _I – V _O	-	1.7	-	Vdc

ELECTRICAL CHARACTERISTICS (V_I = 15 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, -40°C < T_J < +125°C (for MC78LXXAB), 0°C < T_J < +125°C (for MC78LXXAC), unless otherwise noted.)

 $0^{\circ}C < T_{J} < +125^{\circ}C$ (for MC78LXXAC), unless otherwise noted.)

		М			
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = $+25^{\circ}$ C)	Vo	11.5	12	12.5	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ Ma})$ 14.5 Vdc $\leq V_I \leq 27 \text{ Vdc}$ 16 Vdc $\leq V_I \leq 27 \text{ Vdc}$	Reg _{line}		120 100	250 200	mV
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}		20 10	100 50	mV
Output Voltage (14.5 Vdc \leq V _I \leq 27 Vdc, 1.0 mA \leq I _O \leq 40 mA) (V _I = 19 V, 1.0 mA \leq I _O \leq 70 mA)	Vo	11.4 11.4		12.6 12.6	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}		4.2	6.5 6.0	mA
Input Bias Current Change (16 Vdc \leq V _I \leq 27 Vdc) (1.0 mA \leq I _O \leq 40 mA)	ΔI _{IB}			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f∐≤100 kHz)	V _n	-	80	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 15 V $\leq V_I \leq$ 25 V, T _J = +25°C)	RR	37	42	-	dB
Dropout Voltage $(T_J = +25^{\circ}C)$	V _I – V _O	-	1.7	-	Vdc

		MC78L15	MC78L15AC, AB / NCV78L15A		
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	14.4	15	15.6	Vdc
Line Regulation (T _J = +25°C, I _O = 40 mA) 17.5 Vdc ≤ V _I ≤ 30 Vdc 20 Vdc ≤ V _I ≤ 30 Vdc	Reg _{line}		130 110	300 250	mV
Load Regulation (T _J = +25°C, 1.0 mA ≤ I_O ≤ 100 mA) (T _J = +25°C, 1.0 mA ≤ I_O ≤ 40 mA)	Reg _{load}		25 12	150 75	mV
Output Voltage (17.5 Vdc ≤ V _I ≤ 30 Vdc, 1.0 mA ≤ I _O ≤ 40 mA) (V _I = 23 V, 1.0 mA ≤ I _O ≤ 70 mA)	Vo	14.25 14.25		15.75 15.75	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	Ι _{ΙΒ}		4.4	6.5 6.0	mA
Input Bias Current Change (20 Vdc \leq V _I \leq 30 Vdc) (1.0 mA \leq I _O \leq 40 mA)	Δl _{IB}			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f <u>]</u> ≤100 kHz)	Vn	-	90	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 18.5 V ≤ V _I ≤ 28.5 V, T _J = +25°C)	RR	34	39	-	dB
Dropout Voltage ($T_J = +25^{\circ}C$)	V _I – V _O	-	1.7	-	Vdc

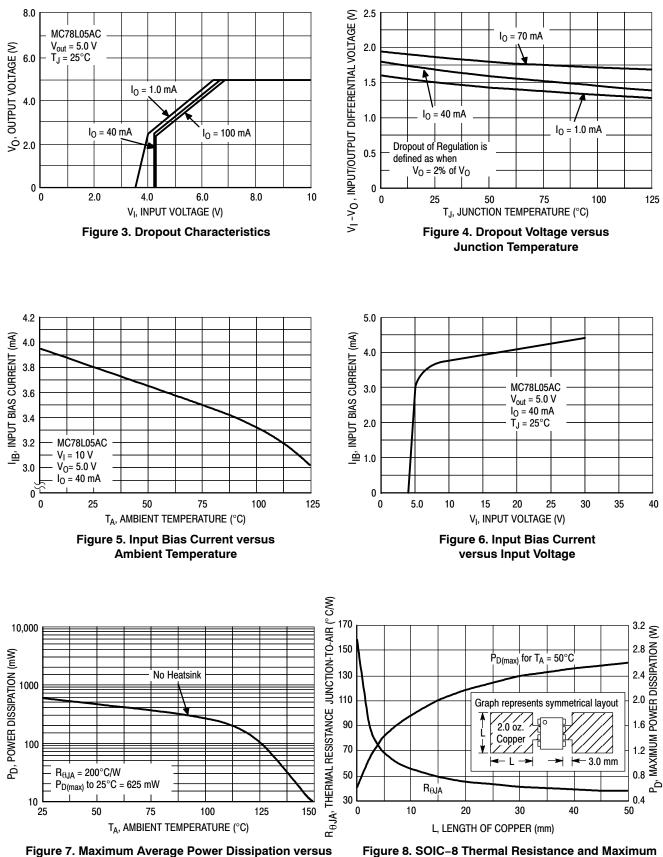
ELECTRICAL CHARACTERISTICS (V_I = 23 V, I_O = 40 mA, C_I = 0.33 μ F, C_O = 0.1 μ F, -40°C < T_J < +125°C (for MC78LXXAB), 0°C < T_J < +125°C (for MC78LXXAC), unless otherwise noted.)

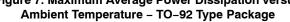
$\textbf{ELECTRICAL CHARACTERISTICS} (V_I = 27 \text{ V}, \text{ I}_O = 40 \text{ mA}, \text{ C}_I = 0.33 \text{ }\mu\text{F}, \text{ C}_O = 0.1 \text{ }\mu\text{F}, 0^\circ\text{C} < \text{T}_J < +125^\circ\text{C}, \text{ unless otherwise noted.})$

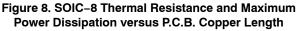
			MC78L18A	0	
Characteristics	Symbol	Min	Тур	Мах	Unit
Output Voltage ($T_J = +25^{\circ}C$)	Vo	17.3	18	18.7	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$ 21.4 Vdc $\leq V_I \leq 33 \text{ Vdc}$	Reg _{line}			0.05	mV
20.7 Vdc ≤ V _I ≤ 33 Vdc 22 Vdc ≤ V _I ≤ 33 Vdc 21 Vdc ≤ V _I ≤ 33 Vdc		-	45 35	325 275	
Load Regulation $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 100 \text{ mA})$ $(T_J = +25^{\circ}C, 1.0 \text{ mA} \le I_O \le 40 \text{ mA})$	Reg _{load}		30 15	170 85	mV
Output Voltage (21.4 Vdc $\leq V_{I} \leq 33$ Vdc, 1.0 mA $\leq I_{O} \leq 40$ mA) (20.7 Vdc $\leq V_{I} \leq 33$ Vdc, 1.0 mA $\leq I_{O} \leq 40$ mA) ($V_{I} = 27$ V, 1.0 mA $\leq I_{O} \leq 70$ mA)	Vo	17.1	-	18.9	Vdc
$(V_{I} = 27 \text{ V}, 1.0 \text{ mA} \le I_{O} \le 70 \text{ mA})$		17.1	-	18.9	
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	Ι _{ΙΒ}		3.1 -	6.5 6.0	mA
Input Bias Current Change (22 Vdc \leq V ₁ \leq 33 Vdc)	ΔI_{IB}				mA
(21 Vdc ≤ V _I ≤ 33 Vdc) (1.0 mA ≤ I _O ≤ 40 mA)				1.5 0.1	
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f⊡≤100 kHz)	V _n	_	150	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 23 V \leq V _I \leq 33 V, T _J = +25°C)	RR	33	48	-	dB
Dropout Voltage ($T_J = +25^{\circ}C$)	V _I – V _O	-	1.7	-	Vdc

			MC78L24AC		
Characteristics	Symbol	Min	Тур	Max	Unit
Output Voltage (T _J = +25°C)	Vo	23	24	25	Vdc
Line Regulation $(T_J = +25^{\circ}C, I_O = 40 \text{ mA})$ 27.5 Vdc $\leq V_I \leq 38$ Vdc 28 Vdc $\leq V_I \leq 80$ Vdc 27 Vdc $\leq V_I \leq 38$ Vdc	Reg _{line}		_ 50 60	300 350	mV
Load Regulation ($T_J = +25^{\circ}C$, 1.0 mA $\leq I_O \leq$ 100 mA) ($T_J = +25^{\circ}C$, 1.0 mA $\leq I_O \leq$ 40 mA)	Reg _{load}		40 20	200 100	mV
$\begin{array}{l} \text{Output Voltage} \\ (28 \text{ Vdc} \leq \text{V}_{I} \leq 38 \text{ Vdc}, \ 1.0 \text{ mA} \leq \text{I}_{O} \leq 40 \text{ mA}) \\ (27 \text{ Vdc} \leq \text{V}_{I} \leq 38 \text{ Vdc}, \ 1.0 \text{ mA} \leq \text{I}_{O} \leq 40 \text{ mA}) \\ (28 \text{ Vdc} \leq \text{V}_{I} \equiv 33 \text{ Vdc}, \ 1.0 \text{ mA} \leq \text{I}_{O} \leq 70 \text{ mA}) \\ (27 \text{ Vdc} \leq \text{V}_{I} \leq 33 \text{ Vdc}, \ 1.0 \text{ mA} \leq \text{I}_{O} \leq 70 \text{ mA}) \end{array}$	Vo	22.8 22.8	-	25.2 25.2	Vdc
Input Bias Current $(T_J = +25^{\circ}C)$ $(T_J = +125^{\circ}C)$	I _{IB}		3.1 -	6.5 6.0	mA
Input Bias Current Change (28 Vdc \leq V _I \leq 38 Vdc) (1.0 mA \leq I _O \leq 40 mA)	Δl _{IB}			1.5 0.1	mA
Output Noise Voltage (T _A = +25°C, 10 Hz ≤ f <u>□</u> ≤100 kHz)	Vn	-	200	-	μV
Ripple Rejection (I _O = 40 mA, f = 120 Hz, 29 V \leq V _I \leq 35 V, T _J = +25°C)	RR	31	45	-	dB
Dropout Voltage $(T_J = +25^{\circ}C)$	V _I – V _O	-	1.7	-	Vdc

$\textbf{ELECTRICAL CHARACTERISTICS} (V_{I} = 33 \text{ V}, I_{O} = 40 \text{ mA}, C_{I} = 0.33 \text{ }\mu\text{F}, C_{O} = 0.1 \text{ }\mu\text{F}, 0^{\circ}\text{C} < T_{J} < +125^{\circ}\text{C}, \text{ unless otherwise noted.})$





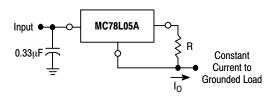


APPLICATIONS INFORMATION

Design Considerations

The MC78L00A Series of fixed voltage regulators are designed with Thermal Overload Protection that shuts down the circuit when subjected to an excessive power overload condition. Internal Short Circuit Protection limits the maximum current the circuit will pass.

In many low current applications, compensation capacitors are not required. However, it is recommended that the regulator input be bypassed with a capacitor if the regulator is connected to the power supply filter with long wire lengths, or if the output load capacitance is large. The



The MC78L00 regulators can also be used as a current source when connected as above. In order to minimize dissipation the MC78L05C is chosen in this application. Resistor R determines the current as follows:

$$I_0 = \frac{5.0 \text{ V}}{\text{B}} + I_{\text{B}}$$

 $I_{IB} = 3.8$ mA over line and load changes

For example, a 100 mA current source would require R to be a 50 $\Omega,$ 1/2 W resistor and the output voltage compliance would be the input voltage less 7 V.

Figure 9. Current Regulator

input bypass capacitor should be selected to provide good high-frequency characteristics to insure stable operation under all load conditions. A 0.33 μ F or larger tantalum, mylar, or other capacitor having low internal impedance at high frequencies should be chosen. The bypass capacitor should be mounted with the shortest possible leads directly across the regulators input terminals. Good construction techniques should be used to minimize ground loops and lead resistance drops since the regulator has no external sense lead. Bypassing the output is also recommended.

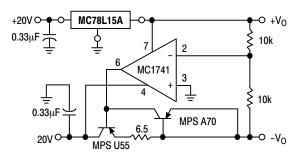


Figure 10. ±15 V Tracking Voltage Regulator

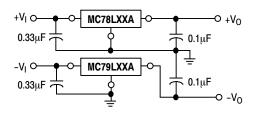


Figure 11. Positive and Negative Regulator

ORDERING INFORMATION

Device	Output Voltage	Operating Temperature Range	Package	Shipping [†]
MC78L05ABDG	5.0 V	$T_{\rm J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOIC-8 (Pb-Free)	98 Units/Rail
NCV78L05ABDG*	5.0 V	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	SOIC-8 (Pb-Free)	98 Units/Rail
MC78L05ABDR2G	5.0 V	$T_{\rm J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel
NCV78L05ABDR2G*	5.0 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L05ABPG	5.0 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92 (Pb-Free)	2000 Units/Bag
NCV78L05ABPG*	5.0 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO–92 (Pb–Free)	2000 Units/Bag
MC78L05ABPRAG	5.0 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO–92 (Pb–Free)	2000 Tape & Reel
NCV78L05ABPRAG*	5.0 V	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	TO–92 (Pb–Free)	2000 Tape & Reel
MC78L05ABPREG	5.0 V	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	TO–92 (Pb–Free)	2000 Tape & Reel
NCV78L05ABPREG*	5.0 V	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Tape & Reel
MC78L05ABPRMG	5.0 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92 (Pb-Free)	2000 Ammo Pack
NCV78L05ABPRMG*	5.0 V	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Ammo Pack
NCV78L05ABPRPG*	5.0 V	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	TO–92 (Pb–Free)	2000 Ammo Pack
MC78L05ACDG	5.0 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	SOIC-8 (Pb-Free)	98 Units/Rail
MC78L05ACDR2G	5.0 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L05ACPG	5.0 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	TO–92 (Pb–Free)	2000 Units/Bag
MC78L05ACPRAG	5.0 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	TO–92 (Pb–Free)	2000 Tape & Reel
MC78L05ACPREG	5.0 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Tape & Reel
MC78L05ACPRMG	5.0 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	TO–92 (Pb–Free)	2000 Ammo Pack
MC78L05ACPRPG	5.0 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	TO–92 (Pb–Free)	2000 Ammo Pack
MC78L08ABDG	8.0 V	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	SOIC-8 (Pb-Free)	98 Units/Rail
MC78L08ABDR2G	8.0 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel

*NCV78L05A, NCV78L12A, NCV78L15A: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable. †For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifica-tions Brochure, BRD8011/D.

ORDERING INFORMATION (continued)

Device	Output Voltage	Operating Temperature Range	Package	Shipping [†]
NCV78L08ABDR2G*	8.0 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L08ABPG	8.0 V	$T_J = -40^\circ$ to $+125^\circ$ C	TO–92 (Pb–Free)	2000 Units/Bag
MC78L08ABPRAG	8.0 V	$T_J = -40^\circ$ to $+125^\circ$ C	TO-92 (Pb-Free)	2000 Tape & Reel
MC78L08ABPRPG	8.0 V	$T_J = -40^\circ$ to $+125^\circ$ C	TO-92 (Pb-Free)	2000 Ammo Pack
MC78L08ACDG	8.0 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	SOIC-8 (Pb-Free)	98 Units/Rail
MC78L08ACDR2G	8.0 V	$T_{\rm J} = 0^{\circ} \text{ to } +125^{\circ} \text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L08ACPG	8.0 V	$T_{\rm J} = 0^{\circ} \text{ to } +125^{\circ} \text{C}$	TO-92 (Pb-Free)	2000 Units/Bag
MC78L08ACPRAG	8.0 V	$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Tape & Reel
MC78L08ACPREG	8.0 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Tape & Reel
MC78L08ACPRPG	8.0 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Ammo Pack
MC78L09ABDG	9.0 V	$T_J = -40^\circ$ to $+125^\circ$ C	SOIC-8 (Pb-Free)	98 Units/Rail
MC78L09ABDR2G	9.0 V	$T_J = -40^\circ$ to $+125^\circ$ C	SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L09ABPRAG	9.0 V	$T_J = -40^\circ$ to $+125^\circ$ C	TO-92 (Pb-Free)	2000 Tape & Reel
MC78L09ABPRPG	9.0 V	$T_J = -40^\circ$ to $+125^\circ$ C	TO-92 (Pb-Free)	2000 Ammo Pack
MC78L09ACDG	9.0 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	SOIC-8 (Pb-Free)	98 Units/Rail
MC78L09ACDR2G	9.0 V	$T_{\rm J} = 0^{\circ} \text{ to } +125^{\circ} \text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L09ACPG	9.0 V	$T_{\rm J} = 0^{\circ} \text{ to } +125^{\circ} \text{C}$	TO-92 (Pb-Free)	2000 Units/Bag
MC78L12ABDG	12 V	$T_J = -40^\circ$ to $+125^\circ$ C	SOIC-8 (Pb-Free)	98 Units/Rail
MC78L12ABDR2G	12 V	$T_J = -40^\circ$ to $+125^\circ$ C	SOIC-8 (Pb-Free)	2500 Tape & Reel
NCV78L12ABDG*	12 V	$T_J = -40^\circ$ to $+125^\circ$ C	SOIC-8 (Pb-Free)	98 Units/Rail
NCV78L12ABDR2G*	12 V	$T_J = -40^\circ$ to $+125^\circ$ C	SOIC-8 (Pb-Free)	2500 Tape & Reel
MC78L12ABPG	12 V	$T_J = -40^\circ$ to $+125^\circ$ C	TO-92 (Pb-Free)	2000 Units/Bag
MC78L12ABPRPG	12 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92 (Pb-Free)	2000 Ammo Pack

*NCV78L05A, NCV78L12A, NCV78L15A: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable. †For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifica-tions Brochure, BRD8011/D.

ORDERING INFORMATION (continued)

Device	Output Voltage	Operating Temperature Range	Package	Shipping [†] 2000 Units/Bag	
NCV78L12ABPG*	12 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92 (Pb-Free)		
MC78L12ACDG	12 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	SOIC-8 (Pb-Free)	98 Units/Rail	
MC78L12ACDR2G	12 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel	
MC78L12ACPG	12 V	$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Units/Bag	
MC78L12ACPRAG	12 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Tape & Reel	
MC78L12ACPREG	12 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Tape & Reel	
MC78L12ACPRMG	12 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Ammo Pack	
MC78L12ACPRPG	12 V	$T_J = 0^\circ \text{ to } +125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Ammo Pack	
MC78L15ABDG	15 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOIC-8 (Pb-Free)	98 Units/Rail	
MC78L15ABDR2G	15 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel	
NCV78L15ABDR2G*	15 V	$T_{\rm J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	SOIC-8 (Pb-Free)	2500 Tape & Reel	
MC78L15ABPG	15 V	$T_{\rm J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92 (Pb-Free)	2000 Units/Bag	
MC78L15ABPRAG	15 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92 (Pb-Free)	2000 Tape & Reel	
MC78L15ABPRPG	15 V	$T_{J} = -40^{\circ} \text{ to } +125^{\circ}\text{C}$	TO-92 (Pb-Free)	2000 Ammo Pack	
MC78L15ACDG	15 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	SOIC-8 (Pb-Free)	98 Units/Rail	
MC78L15ACDR2G	15 V	$T_J = 0^\circ$ to +125°C	SOIC-8 (Pb-Free)	2500 Tape & Reel	
MC78L15ACPG	15 V	$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Units/Bag	
MC78L15ACPRAG	15 V	$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Tape & Reel	
MC78L15ACPRPG	15 V	$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Ammo Pack	
MC78L18ABPG	18 V	$T_J = -40^\circ$ to $+125^\circ$ C	TO-92 (Pb-Free)	2000 Units/Bag	
MC78L18ACPG	18 V	$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Units/Bag	
MC78L18ACPRAG	18 V	$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Tape & Reel	
MC78L18ACPRMG	18 V	$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Ammo Pack	

*NCV78L05A, NCV78L12A, NCV78L15A: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q100 Qualified and PPAP Capable. †For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifica-

tions Brochure, BRD8011/D.

ORDERING INFORMATION (continued)

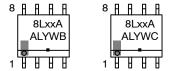
Device	Output Voltage	Operating Temperature Range	Package	Shipping [†]	
MC78L18ACPRPG	18 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Ammo Pack	
MC78L24ABPG	24 V	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Units/Bag	
NCV78L24ABPRPG*	24 V	$T_J = -40^\circ \text{ to } +125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Units/Bag	
MC78L24ACPG	24 V	$T_J = 0^\circ$ to +125°C	TO-92 (Pb-Free)	2000 Units/Bag	
MC78L24ACPRAG	24 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Tape & Reel	
MC78L24ACPRPG	24 V	$T_J = 0^\circ \text{ to } + 125^\circ \text{C}$	TO-92 (Pb-Free)	2000 Ammo Pack	

*NCV78L05A, NCV78L12A, NCV78L15A: T_{low} = -40°C, T_{high} = +125°C. Guaranteed by design. NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable.

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

MARKING DIAGRAMS

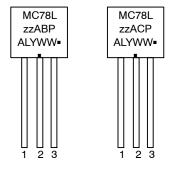




XX =	05,	08,	09,	12,	or	15
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- A = Assembly Location
- L = Wafer Lot
- Y = Year
- W = Work Week
- B, C = Temperature Range
- = Pb-Free Package





zz = 05, 08, 09, 12, 15, 18 or 24

A = Assembly Location

L = Wafer Lot

Y = Year

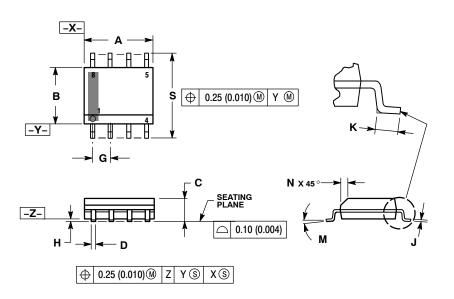
WW = Work Week

= Pb-Free Package

(Note: Microdot may be in either location)

PACKAGE DIMENSIONS

SOIC-8 NB CASE 751-07 **ISSUE AK**



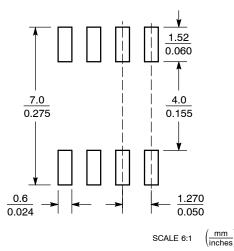
NOTES:

- NOTES:
 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETER.
 DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE

- MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
 DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.
 751–01 THRU 751–06 ARE OBSOLETE. NEW STANDARD IS 751–07.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	4.80	5.00	0.189	0.197	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.053	0.069	
D	0.33	0.51	0.013	0.020	
G	1.27 BSC		0.050 BSC		
н	0.10	0.25	0.004	0.010	
J	0.19	0.25	0.007	0.010	
к	0.40	1.27	0.016	0.050	
м	0 °	8 °	0 °	8 °	
Ν	0.25	0.50	0.010	0.020	
S	5.80	6.20	0.228	0.244	

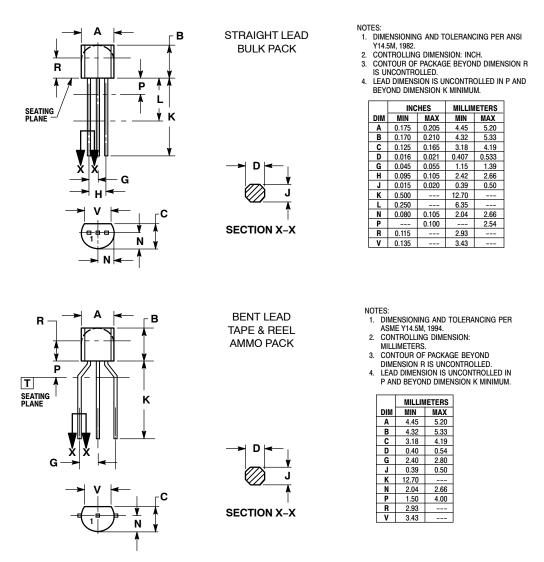
SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 029-11 ISSUE AM



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